5 Getting Started with the SoCKit Board

Users can reference the document SoCKit_GSG.pdf found in the SoCKit System CD manual folder.This guide contains a quick overview of the hardware and software setup including step-by-step procedures from installing the necessary software tools to using the SoCKit board.

The main topics that this guide covers are listed below:

1. Software Installation: Installing Quartus II and EDS.
2. Development Board Setup: Powering on the SoCKit.
3. Perform FPGA System Test: Downloading an FPGA SRAM Objective File (.sof).
4. Running Linux on SoCKit Board.

6 Starting Your First FPGA Design

Users can reference the document My first FPGA.pdf found in the SoCKit System CD manual folder.

This document describes the complete FPGA design flow, including:

1. Creating a new Quartus II project.
2. Adding user logic and utilizing mega-core IPs.
3. Downloading an .sof file to the FPGA to view the result.

7 Getting Help

For further discussion, support, and resources, please go to:

http://sockit_support.terasic.com

http://rocketboards.org

If you encounter any problem, please contact us below

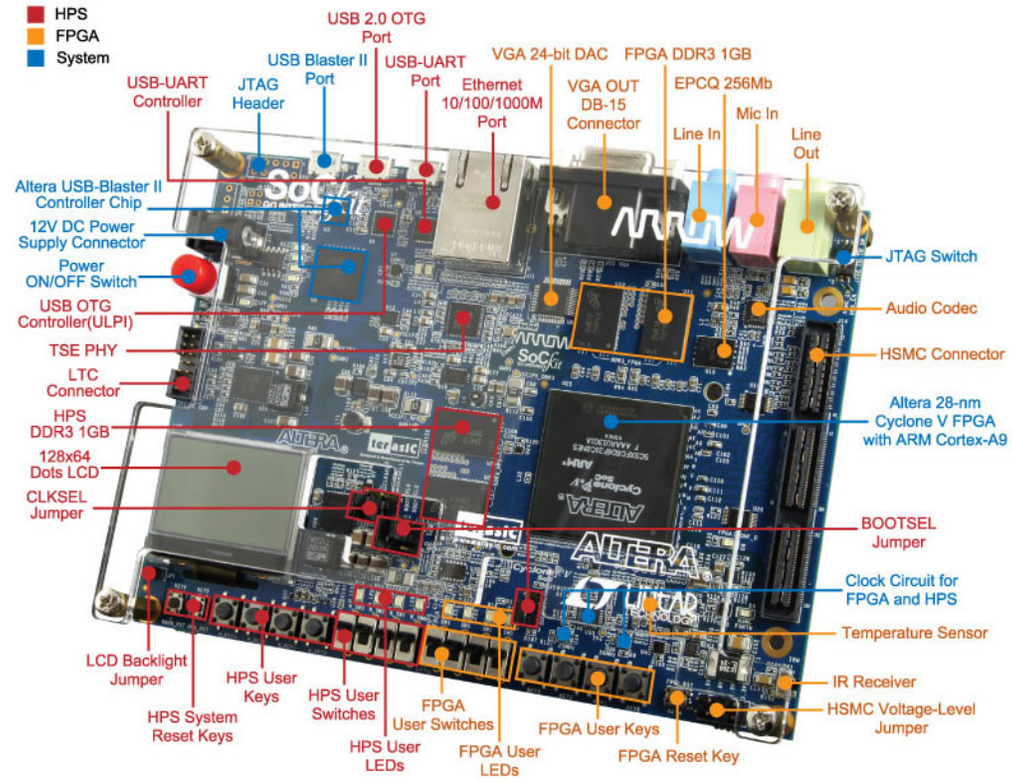
Email: support@terasic.com Tel: +886-3-575-0880

CPT-3164-00

www.terasic.com

SoCKit

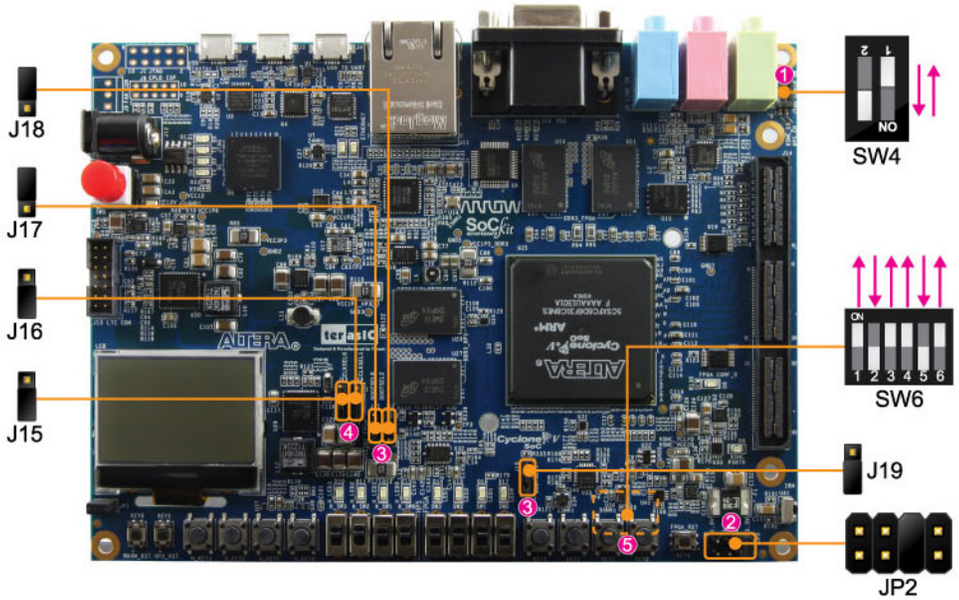
Quick Start Guide



1 What's in the Box?

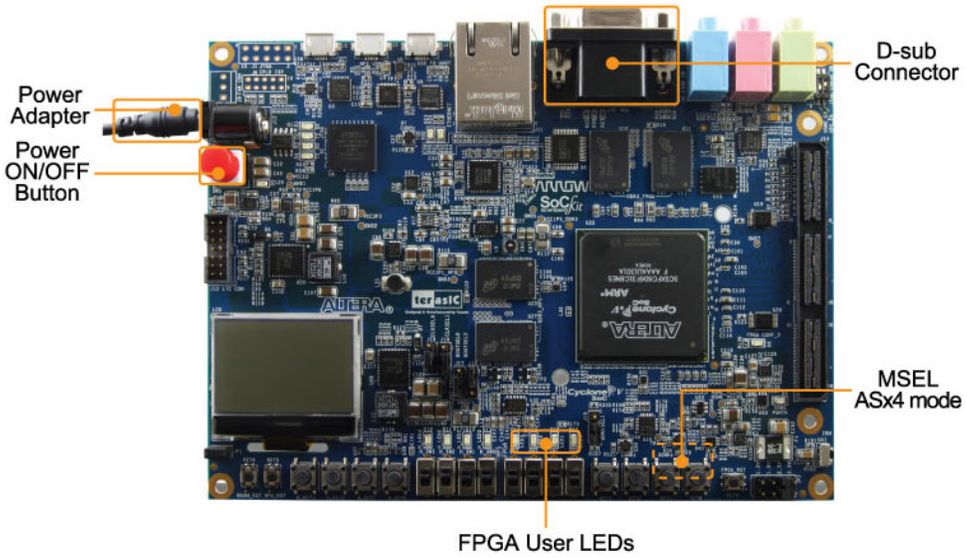
1. SoCKit Board
2. SoCKit Quick Start Guide
3. Power DC Adapter (12V)
4. Ethernet Cat 5e Cable
5. Type A to Micro B USB Cable x2

2 Default Jumper and Switch Settings



Item	Component	Function	Setting	Description
1	SW4	JTAG Chain Select	Set HPS_EN to OFF Set HSMC_EN to ON	Enable HPS Bypass HSMC
2	JP2	Adjust the I/O Standard of the FPGA/HSMC pins	Short Pin 5 and 6	The default is 2.5V
3	J17 J19 J18	Select the boot source for the HPS	BOOTSEL0 Logic 1: Short Pin 1 and 2 BOOTSEL1 Logic 0: Short Pin 2 and 3 BOOTSEL2 Logic 1: Short Pin 1 and 2	3.0 V SD/MMC Flash memory
4	J15 J16	Select the clock settings for the HPS	CLKSEL0 Logic 0: Short Pin 2 and 3 CLKSEL1: Short Pin 2 and 3 CLKSEL1 Logic 0: Short Pin 2 and 3	Settings are bootsel selection dependent
5	SW6	FPGA Configuration Mode Switch	Set SW6.1~SW6.5 to "01001"	ASx4 mode

3 Perform Power-on Test



1. Connect the power adapter to the power jack of the SoCKit.
2. Press the power on button.
3. Make sure SW6 is set to ASx4 mode (SW6.1~SW6.5 "01001")
4. All the FPGA User LEDs are flashing.
5. Connect a VGA monitor to D-sub connector(J10) and the VGA monitor will display the board image.

4 Contents of the SoCKit System CD

Users can download the SoCKit System CD from this link:

http://sockit_support.terasic.com

SoCKit System CD Contents	
Directory Name	**Contents**
User_Manual	Contains the SoCKit documentations
Demonstrations	Contains design examples for SoCKit application
Datasheet	Contains the datasheets of the components on the SoCKit
Schematic	Contains the schematics of SoCKit
Tools	Contains the design and testing tools for SoCKit